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Lab-8

PIC microcontroller Timer Programming using assembly language

Objective:

In this lab students will learn

- How many timers are there in PIC18f4550
- How timers are used for introducing delays.

Theory

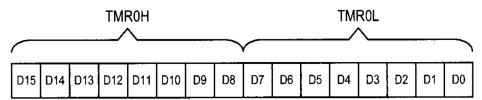
The PIC18 has two to five timers depending on the family member. They are referred to as Timers 0, 1, 2, 3, and 4. They can be used either as timers to generate a time delay or as counters to count events happening outside the microcon-

troller.

Basic registers of the timer

Many of the PIC18 timers are 16 bits wide. Because the PIC18 has an 8-bit architecture, each 16-bit timer is accessed as two separate registers of low byte (TMRxL) and high byte (TMRxH). Each timer also has the TCON (timer control) register for setting modes of operation. Next, we discuss each timer separately.

Timer0 register and programming



Each timer has a control register, called TCON, to set the various timer operation modes. T0CON is an 8-bit register used for control of Timer0. The bits for T0CON are shown in Figure 9-2.

TOCS (Timer0 clock source)

This bit in the T0CON register is used to decide whether the clock source is internal (Fosc/4) or external. If T0CS = 0, then the Fosc/4 is used as clock source. In this case, the timers are often used for time delay generation. See Example 9-1. If T0CS = 1, the clock source is external and comes from the RA4/T0CKI, which is pin 6 on the DIP package of PIC1818F4580/4520. When the clock source comes from an external source, the timer is used as an event counter. We will discuss that option in the next section. See Example 9-2.

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TMR0ON	T08BIT	T0CS	T0SE	PSA	T0PS2	T0PS1	T0PS0
TMR0ON	D7	Timer0 (ON and OF	F control b	it		
		1 = Enal	ole (start) T	imer0			
		0 = Stop	Timer0				
T08BIT	D6		+	selector bit			
			,	gured as an			
				gured as a	16-bit time	r/counter.	
T0CS	D5		lock source		DOCIEL '		
				from RA4/7	_	اسمغمالنم	
TOSE	Τ.4		•	Fosc/4 fron	n ATAL OS	ciliator)	
IUSE	D4		ource edge	-to-L transi	ition on TO	CKLnin	
				-to-L transi		_	
PSA	D3			signment b		CICI pin	
1011	DJ	-		•		r.	
		1 = Timer0 clock input bypasses prescaler.0 = Timer0 clock input comes from prescaler output.					
T0PS2:T0	PS0 D2D1			scaler selec	-	1	
	0 0 0		-	lue (Fosc /			
	0 0 1	= 1:4	Prescale va	lue (Fosc /	4 / 4)		
	0 1 0	= 1:8	Prescale va	lue (Fosc /	4 / 8)		
				lue (Fosc /	-		
				lue (Fosc /	•		
				lue (Fosc /			
				lue (Fosc /	-		
	111:	= 1:256	Prescale va	lue (Fosc /	4 / 256)		

16-bit timer programming

The following are the characteristics and operations of 16-bit mode:

- 1. It is a 16-bit timer; therefore, it allows values of 0000 to FFFFH to be loaded into the registers TMR0H and TMR0L.
- 2. After TMR0H and TMR0L are loaded with a 16-bit initial value, the timer must be started. This is done by "BSF TOCON, TMR0ON" for Timer0.
- 3. After the timer is started, it starts to count up. It counts up until it reaches its limit of FFFFH. When it rolls over from FFFFH to 0000, it sets HIGH a flag bit called TMR0IF (timer interrupt flag, which is part of the INTCON register). This timer flag can be monitored. When this timer flag is raised, one option would be to stop the timer.
- 4. After the timer reaches its limit and rolls over, in order to repeat the process, the registers TMR0H and TMR0L must be reloaded with the original value, and the TMR0IF flag must be reset to 0 for the next round.

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Lab Ex	kercise				
portion	ns high	ng program, we are c and low) on the POI program.	creating a square wave of 50% duty RTB.5 bit. Timer0 is used to genera	cycle (with equal ate the time delay.	
HERE	MOVLW MOVWF MOVWF BCF	0x08 T0CON 0xff TMR0H	;PB5 as an output ;Timer0,16-bit,int clk,no ;load T0CON reg. ;TMR0H = FFH, the high by ;load Timer0 high byte ;TMR0L = F2H, the low byte ;load Timer0 low byte ;clear timer interrupt fla ;toggle PB5	te e	
AGAIN	BTFSS BRA	TOCON, TMROON INTCON, TMROIF AGAIN TOCON, TMROON HERE	<pre>;start Timer0 ;monitor Timer0 flag until ;it rolls over</pre>	L	
Also ca	lculate	the delay introduced.		[5]	

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_			
Calculate the fueguency of gove		manatad at min DODT	D 5 Chary Cimulations
Calculate the frequency of squ	are wavelorm ge	enerated at pin POK1	
on Proteus.			[5]
		Cycles	
	BCF TRISB,5 MOVLW 0x08		
	MOVWF TOCON BCF INTCON,	TMR0IF	
HERE	MOVLW 0xFF	1	
	MOVWF TMROH MOVLW -D'48'	1 1	
	MOVWF TMROL	1	

ю.			Cycles
	BCF	TRISB,5	
	MOVLW	0x08	
	MOVWF	TOCON	
	BCF	INTCON, TMR0IF	
HERE	MOVLW	0xFF	1
	MOVWF	TMROH	1
	MOVLW	-D'48'	1
	MOVWF	TMROL	1
	CALL	DELAY	1
	BTG	PORTB, 5	1
	BRA	HERE	1
;	delay us	ing Timer0	
DELAY	BSF	TOCON, TMROON	1
AGAIN	BTFSS	INTCON, TMR0IF	1
	BRA	AGAIN	1
	BCF	TOCON, TMROON	1
	BCF	INTCON, TMR0IF	1
	RETUR	N	_1
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Write a program to generate a square wavef Show Simulations on Proteus and verify you		nsec on pin PORTB.3.
Generate a square waveform of 3KHz frequence and verify your calculations.	ency on pin PORTB.3. Sh	now Simulations on [5]

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Compare the 8-bit and 16-bit timer mode of	PIC microcontroller	[2]
What is the minimum frequency that you ca	n generate using 16-bit timer mode?	[3]
Conclusion		[3]