

**Assignment 1**

**CLO1-Introduction to Embedded Systems**

**Instructions:**

**This is an individual task.**

**Only hand written submission will be accepted.**

**No late submission will be accepted.**

**Marks: 5+5**

1. What is the difference between RISC and CISC architecture?
2. Discuss RISC-V architecture in detail. What is the difference between soft-core and hard-core RISC-V implementations?